

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent No.: 7,432,769 B2

Applicant(s): Isami KATO et al.

Issued: October 7, 2008

Customer No.: 85775

For: OSCILLATOR CIRCUIT

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

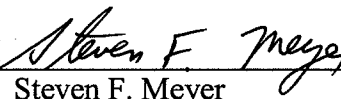
Sir:

Attached in duplicate is Form PTO-1050, with at least one copy being suitable for printing.

- ☐ The error was the fault of the Patent and Trademark Office, no fee is required.
- ☒ The error was not the fault of the Patent and Trademark Office, please charge the requisite fee of \$100 to Deposit Account No. 13-4500, Order No. 1004378.52470.
- ☒ The Commissioner is hereby authorized to charge any additional fees which may be required by this paper, or credit any overpayment to Deposit Account No. 13-4500, Order No. 1004378.52470.

Respectfully submitted,  
Locke Lord Bissell & Liddell LLP

Dated: September 18, 2009

  
Steven F. Meyer  
Registration No. 35,613

Correspondence Address:

Address Associated With Customer Number:  
**85775**

(212) 415-8600 (Telephone)  
(212) 303-2754 (Facsimile)

**UNITED STATES PATENT AND TRADEMARK OFFICE**  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,432,769 B2  
ISSUED : October 7, 2008  
INVENTOR(S): Isami KATO et al.  
TITLE: OSCILLATOR CIRCUIT

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**In the Specification**

Column 1, line 10, please delete "In an AM and an FM receivers," and insert therefore - - In AM and FM receivers,--;

Column 2, line 15, please delete "the capacitance of variable capacitance element" and insert therefore - - the capacitance of the variable capacitance element--;

Column 2, lines 18-19, please delete "the oscillating unit comprises a first and second MOSFETs," and insert therefore - - the oscillating unit comprises first and second MOSFETs,--;

Column 2, lines 28-29, please delete "This configuration enables oscillating unit comprising a first and second MOSFETs," and insert therefore - - This configuration enables an oscillating unit comprising first and second MOSFETs,--;

Column 2, line 30, please delete "variable capacitance element can be formed on" and insert therefore - - variable capacitance element to be formed on--;

Column 3, line 15, please delete "embodiment;" and insert therefore - - embodiment; and--;

Column 3, line 23, please delete "reference to the accompanying drawing" and insert therefore - - reference to the accompanying drawings--;

Column 3, line 25, please delete "used for an AM and an FM receivers" and insert therefore - - used for an AM and an FM receiver--;

Column 3, line 58, please delete "the above described MOSFET 12 and 13," and insert therefore - - the above described MOSFETs 12 and 13,--;

Column 4, lines 51-52, please delete "the PLL synthesizer 21 compares between 6 the phase" and insert therefore - - the PLL synthesizer 21 compares between the phase--;

Column 4, line 55, please delete "and control the oscillation frequency" and insert therefore - - and controls the oscillation frequency--;

Column 5, line 35, please delete "MOSFET 12 and 13," and insert therefore - - MOSFETs 12 and 13,--;

Column 5, lines 61-62, please delete "assembly costs of an AM and an FM receivers." and insert therefore - - assembly costs of AM and FM receivers.--;

Column 6, line 5, please delete "to any oscillator circuits using an inductance and a capacitor;" and insert therefore - - to any oscillator circuit using an inductance and a capacitor;--;

Column 6, lines 11-12, please delete "but can be any circuits so long as controlling to set the frequency" and insert therefore - - but can be any circuit so long as it controls the setting of the frequency--;

Column 6, line 18, please delete "purpose IC for an AM and an FM receivers," and insert therefore - - purpose IC for AM and FM receivers,--; and

Column 16, line 29, please delete "conductor IC more compact." and insert therefore - - conductor IC more compactly.--.

#### **In the Claims**

In Claim 4, column 7, line 36, please delete "of the oscillating unit by outputting a control voltage" and insert therefore - - of the voltage controlled oscillator by outputting a control voltage--; and

In Claim 20, column 10, lines 3-4, please delete "the output of the second frequency divider circuit" and insert therefore - - the output of the second frequency divider circuit--.

MAILING ADDRESS OF SENDER:

PATENT NO. 7,432,769 B2

Locke Lord Bissell & Liddell LLP

NO. OF ADD'L. COPIES

3 World Financial Center  
New York, NY 10281-2101

@ 30¢ PER PAGE